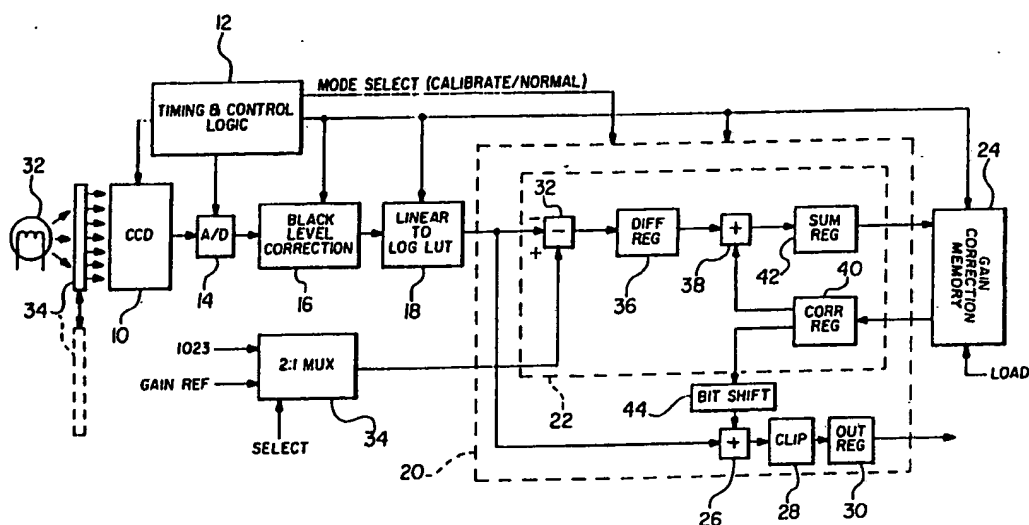


## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<b>(21) International Application Number:</b> PCT/US91/03181 <b>(22) International Filing Date:</b> 8 May 1991 (08.05.91)  <b>(30) Priority data:</b> 522,334                      11 May 1990 (11.05.90)                      US  <b>(71) Applicant:</b> EASTMAN KODAK COMPANY [US/US]; 343 State Street, Rochester, NY 14650 (US).  <b>(72) Inventors:</b> COOK, William, A. ; 62 Springbrook Circle, Ro- chester, NY 14606 (US). D'LUNA, Lionel, J. ; 38 Ly- nette Drive, Rochester, NY 14616 (US). PARULSKI, Kenneth, A. ; 14 Catalpa Road, Rochester, NY 14617 (US).  <b>(74) Agent:</b> WOODS, David, M.; 343 State Street, Rochester, NY 14650-2201 (US).		<b>(81) Designated States:</b> AT (European patent), BE (European patent), CH (European patent), DE (European patent), DK (European patent), ES (European patent), FR (Eu- ropean patent), GB (European patent), GR (European patent), IT (European patent), JP, LU (European pa- tent), NL (European patent), SE (European patent).  <b>Published</b> <i>With international search report.</i> <i>Before the expiration of the time limit for amending the</i> <i>claims and to be republished in the event of the receipt of</i> <i>amendments.</i>

**(54) Title:** METHOD AND APPARATUS FOR COMPENSATING FOR SENSITIVITY VARIATIONS IN THE OUTPUT OF A SOLID STATE IMAGE SENSOR

**(57) Abstract**

A correction circuit (20) processes digitized signals from an image sensor (10) and generates gain correction values to compensate for variations in the output of the sensor. While imaging a gain calibrating object (34), the sensor is operated in a calibration mode in which a plurality of calibration values are generated that pertain to each photosite. The digitized calibration values are transformed into log space for processing by a gain level averaging circuit (22). The log calibration signals are first subtracted from a reference corresponding to a maximum expected signal value. The difference signals are serially accumulated by means of pair of registers (36, 42) and an adder (38), and the sum is stored in a gain memory (24). In a subsequent normal operating mode, the summed signals for each photosite are retrieved from the gain memory (24) and bit-shifted to form an average correction value for each photosite. The correction values are applied to an adder (26) in synchronism with sensor signals from like photosites and added therewith in log space to provide gain compensation.

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METHOD AND APPARATUS FOR COMPENSATING  
FOR SENSITIVITY VARIATIONS IN THE  
OUTPUT OF A SOLID STATE IMAGE SENSOR

5 Technical Field

This invention relates to signal processing of image signals from an image sensor, and more particularly, to a method and apparatus for generating correction values to account for  
10 variations in the sensitivity of different photosites on a solid state image sensor.

Background Art

Variations in photodiode dark current and sensitivity as well as light source non-uniformities  
15 can cause noticeable degradation in the quality of scanned images from a solid state sensor. These variations can be compensated by calibrating the system with no illumination upon the sensor to determine an average black level, and by calibrating  
20 the system with the sensor under full illumination to determine an average maximum (white) value. It is further well known to perform a calibration with regard to each sensor photosite, as shown in the following two examples. In U.S. Patent No.  
25 4,602,291, the "dark current" from the imager array (i.e., the signal obtained when no light strikes the array) is directed via an analog/digital converter to an offset memory, which stores the dark current charge from each photosite in digital form. The  
30 offset value is subsequently processed with a white level value from each photosite (i.e., the signal obtained when a uniform illumination strikes the imager) and the difference thereof is stored in a gain memory for each photosite. In U.S. Patent  
35 4,760,464, a white value for each picture element is

obtained by scanning a white substrate a plurality of times and storing the data obtained for each scan, and constantly replacing a previous white value by a present white value when the present  
5 value is greater than the previous level. Correction is then effected using the maximum values.

In the calibration mode described in U.S. Patent No. 4,343,021, the sensor is presented with a  
10 field of uniform brightness. As a particular sensor element is scanned, the raw video signal is applied to a multiplier and multiplied by a correction coefficient. A comparator then decides whether the real time, processed multiplier signal is greater or  
15 less than a reference signal. A register, which temporarily stores the correction coefficient, is then either incremented or decremented and the adjusted correction coefficient value is returned to its memory location. The next time the image  
20 element is scanned, the coefficient is again drawn from memory and applied to both the multiplier and the indexing register, and the aforementioned process is repeated. After many further passes, the correction coefficient is altered in such a way that  
25 the processed video data approaches the reference signal.

Each of the aforementioned systems have certain disadvantages. The first system, disclosed in the '291 patent, calculates coefficients from  
30 only one scan. Such data may, however, contain errors caused by dust particles or surface flaws on the reference object. The second reference, the '464 patent, scans a plurality of times but skews every adjustment toward the maximum variation  
35 observed, in effect tending to clamp to the largest

noise signal. The latter reference, the '021 patent, achieves a convergence toward a reference value but at the expense of a relatively complex system involving multipliers and comparators.

5 Disclosure of Invention

Unlike the approaches taken by the prior art, we have found certain advantages in doing as much of the calibration as possible in a logarithmically-quantized signal space. For one  
10 thing, a multiplication step becomes a simple addition. This is of more than passing significance when it is realized that such circuits are desirably implemented in a digital integrated circuit, where multipliers are large and, compared to adders,  
15 consume a large area on the chip. Moreover, any chip-saving simplification reduces cost and encourages ordinarily complex features like element-by-element correction.

As a result, a correction circuit  
20 implemented according to the invention provides compensation for illumination and sensitivity variations appearing in image values on an element-by-element basis. That is, a plurality of sample values are generated from each photosite  
25 while the image sensor images an object of uniform transmittance (or reflectance). The calibration values are then converted to logarithmic calibration signals. A logarithmic correction value is generated for each photosite from the plurality of  
30 logarithmically-quantized calibration signals corresponding to each photosite. The correction values are then stored and, when the image sensor is scanning a normal object, the resultant image values are altered by applying the stored correction values  
35 to the scanned image values. Each correction value

therefore pertains to the photosite producing the image value. In a specific embodiment of the invention, the correction values are based upon an average of the plurality of logarithmic calibration  
5 signals corresponding to each photosite and each alteration is completed by adding the log correction value to the log image value.

Brief Description of the Drawings

The invention will be described with  
10 reference to the drawing, which shows a block diagram of a video correction circuit according to the invention.

Best Mode for Carrying Out the Invention

A generic electronic imaging system is  
15 shown in part in the Figure to illustrate a typical application of the invention, but it will be readily understood that the present invention can be employed with any type of photoelectronic scanning system that exhibits sensitivity variations due to  
20 such factors as photosite variability or light source non-uniformity. The latter is a particular problem where the system itself provides illumination for scanning an object, such as provided by a linear scanner for illuminating a film  
25 transparency. Furthermore, the present description will be directed in particular only to elements forming part of, or cooperating more directly with, the present invention. Elements of the scanning system not specifically shown or described herein  
30 may be selected from those known in the art.

The electronic imaging system shown in the Figure includes a charge-coupled device (CCD) image sensor 10 driven by clock signals from a timing and control logic section 12. The output of the image  
35 sensor is a sequence of light values bearing the

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aforementioned variations due to imager sensitivity variations and/or light source non-uniformity. The light values are digitized by an analog-to-digital converter 14 and applied to a black level correction circuit 16. The circuit 16 includes a conventional black reference generator and a conventional black reference clamp, which are used to establish a stable sensor black reference value for the entire image. The black reference is an average thermal dark current noise correction offset value for the sensor, which, unless removed from the signals, will corrupt all subsequent adjustments and corrections. The black reference circuit 16 is initially enabled by the timing and control logic section 12 to collect a sample of dark current signal values and to calculate the average black reference therefrom. As the light values are subsequently processed, the average black reference value is subtracted from each light value in the circuit 16.

The output of the black level correction circuit 16 is transformed into a logarithmically quantized space by a linear-to-log ROM look up table 18. The logarithmic signals are then applied to a gain correction circuit 20. Gain correction operates in two modes: a calibration mode and a normal mode. The image sensor 10 is accordingly driven to provide either a sequence of calibration values (during the calibration mode) while the photosites are subject to uniform illumination or a sequence of image values (during the normal mode) while the photosites are subject to object illumination. The gain control circuit 20 is operated by a "mode select" signal from the timing and control logic section 12 to accordingly process the pixel values. The gain correction values for

the photosites are first computed in a gain level averaging circuit 22 and stored in a gain correction memory 24 in the calibration mode. In the normal mode, the gain correction values are read from the gain correction memory 24 and added to the image value in a correction adder 26. The corrected image values are processed by the clipping circuit 28 and latched into the output register 30. Since the addition by the adder 26 is being performed in log space, the image values are being scaled in a multiplication-type operation.

During the calibration mode, the average of the ratio of the maximum expected signal value to the sensor output under full illumination is calculated. The correction values are computed in the gain correction circuit 20 by averaging a plurality of values from each photosite on the sensor 10 with the sensor illuminated by a light source 32 through a defocused "Dmin" filter 34, that is, a filter having a density corresponding to the minimum density of a nominal transparency material, which serves as a gain calibration object.

(Alternatively, the gain calibration object could be a clear "open gate" opening to the illumination source, or could be a uniform substrate when scanning reflection materials.) More particularly, 256 values from each photosite are averaged. The averaging is completed in two stages. First, each logarithmic signal from the log look-up table 18 is applied to a subtractor 32 to obtain the log difference (i.e., the ratio in linear space) with a log reference value provided by a multiplexer 34. The subtracted value, which is latched into a difference register 36, represents the difference between the "desired" log space gain calibration

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signal level and the actual log space gain calibration signal level or, in linear space, the ratio of these two quantities. In the second stage of the averaging process, the difference value in the difference register 36 is applied to the adder 38 which sums the difference value with a "running sum" value from a correction register 40. The summed result is latched into a sum register 42.

The reference value is selected by the multiplexer 34 from either a reference decimal value of 1023 or a programmable value "GAINREF" supplied by an external processor (not shown). The ordinary, and default, reference value is 1023 (which relates to a 10 bit input to the subtractor 32 from the look up table 18). By choosing the reference value to be 1023, the value stored in the difference register 36 will always be positive, so the subsequent "running average" value will always be a positive value. With the present embodiment, if the programmable reference "GAINREF" is selected, the user must ensure that the signal levels from the sensor do not exceed the reference value, since the gain averaging circuit 22 is specifically designed to add only positive gain reference values. (It should be understood, however, that the circuit could be modified in known ways to handle negative values as well as positive values. Likewise, the reference value relates directly to the bit capacity of the circuit, and will be different for different bit length inputs.)

The gain correction memory 24 interfaces with the correction register 40 and the sum register 42, writing into the correction register 40 and reading from the sum register 42. The memory 24 needs to be addressed by the logic section 12 so



that the proper "running sum" correction value is loaded into the correction register 40 at the proper time, and the resulting "sum" value from the sum register 40 is written back into the same memory location in the gain correction memory 24. In this manner the new sum value overwrites the previous "running sum" value for the particular photosite. Once the correction values for each sensor photosite from 256 calibration-mode repetitions have been summed, the output of the correction register 40 will equal the proper gain correction sums, and the gain calibration mode is complete.

In the normal mode of operation, the gain calibration object 34 is removed from the light path and the normal object to be scanned is inserted into the light path. The gain corrections stored in the gain correction memory 24 for each photosite in the image sensor 10 are accessed as each corresponding image value is applied to the correction adder 26. Accordingly, a unique gain correction for each photosite is used to alter (by adding to, or, in linear space, multiplying with) the image value developed for that photosite. In the preferred practice of the invention, the gain adder 38 combines a 10 bit difference value from the difference register 36 with a 16 bit "running sum" value from the correction register 40, providing a 16 bit sum to the sum register 42. Consequently, the 8 most significant bits (MSBs) of the "running sum" values represents, after 256 adds, the proper gain correction value, that is, the average of 256 adds. In the normal mode of operation, only the 8 MSBs need to be applied from the memory 24. The averaging is obtained by the bit shift circuitry 44, which represents the appropriate hard-wiring between

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the correction register 40 and the adder 26 to accomplish an 8 bit shift.

In the preferred embodiment, the correction signals are limited to a maximum value of  $1/4$  of the full range density signal, which corresponds to 0.75 density units. This is because an 8 bit correction value is combined with a 10 bit image value in the correction adder 26, and correction values greater than  $1/4$  of the full range image value will overflow the gain averaging circuit 22. Moreover, the log calibration values input during the calibration mode must not differ from the value provided by the multiplexer 34 by more than 256 code values. Obviously, these limitations are not aspects of the invention, but of the particular circuit used to implement the invention.

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What is Claimed is:

1. A correction circuit for generating correction values to compensate for variations appearing in image values derived from an image  
5 sensor (10) having a plurality of discrete photosites, said sensor operable in a calibration mode to produce a plurality of calibration values from each photosite while the sensor images a gain calibration object (34), said circuit comprising:  
10 means (18) for converting the calibration values to logarithmic calibration signals;  
means (22) for generating a logarithmic correction value for each photosite from the  
15 plurality of logarithmic calibration signals corresponding to each photosite;  
means (24) for storing said logarithmic correction values; and  
means (40, 44) for applying said stored  
20 correction values to the alteration of the image values whereby each correction value pertains to the photosite producing the image value.
2. A circuit as claimed in Claim 1 wherein said correction value generating means (22) bases  
25 the correction value upon an average of the plurality of logarithmic calibration signals corresponding to each photosite.
3. A circuit as claimed in Claim 1 wherein said correction value generating means (22) includes  
30 means (32) for subtracting each log calibration signal from a reference value corresponding to a maximum expected signal value and means (38) for generating said log correction value from the resultant signals.
- 35 4. A circuit as claimed in Claim 1 wherein

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said image sensor (10) is operable in a normal mode to produce image values from the photosites while the sensor is subject to object illumination, said circuit including means (18) for converting the  
5 calibration values and the image values to logarithmic calibration signals and logarithmic image signals, respectively, means (26) operable in log space for altering the image values; and wherein said means (40, 44) applies said stored correction  
10 values to said altering means whereby each correction value pertains to the photosite producing the image value.

5. A circuit as claimed in Claim 4 wherein said correction value generating means (22) bases  
15 the correction value upon an average of the plurality of logarithmic calibration signals corresponding to each photosite, generating therefrom an average correction value.

6. A circuit as claimed in Claim 5 wherein  
20 said altering means (26) adds the average correction value to each image value.

7. A circuit as claimed in Claim 4 wherein said correction value generating means (22) includes means (32) for subtracting each log calibration  
25 signal from a reference value corresponding to a maximum expected signal value and means for generating said log correction value from the resultant signals.

8. A method of compensating for variations  
30 in the output of an image sensor (10), comprising the steps of:

imaging a gain calibration object (34)  
upon the image sensor (10);

generating a plurality of calibration  
35 values pertaining to each photosite on the image

sensor;

digitizing the calibration values;  
transforming the digitized values into  
log signals;

5               generating a log correction value for  
each photosite from the plurality of values  
pertaining to each photosite;

storing the log correction values;  
applying the stored correction values

10 in log space to image values subsequently generated  
from the image sensor (10) to compensate for the  
variations.

9. A method as claimed in Claim 8 wherein  
the step of generating a log correction value  
15 includes the steps of:

generating a reference value  
corresponding to a maximum expected signal value;  
subtracting each log signal from the  
reference value; and

20               accumulating the subtracted log signals  
for each photosite to form the log correction value  
for each photosite.

10. A method as claimed in Claim 9 wherein  
the step of applying the stored correction values  
25 includes the steps of bit shifting each stored  
correction value to effect an average and adding the  
averaged correction values to image values from like  
photosites.

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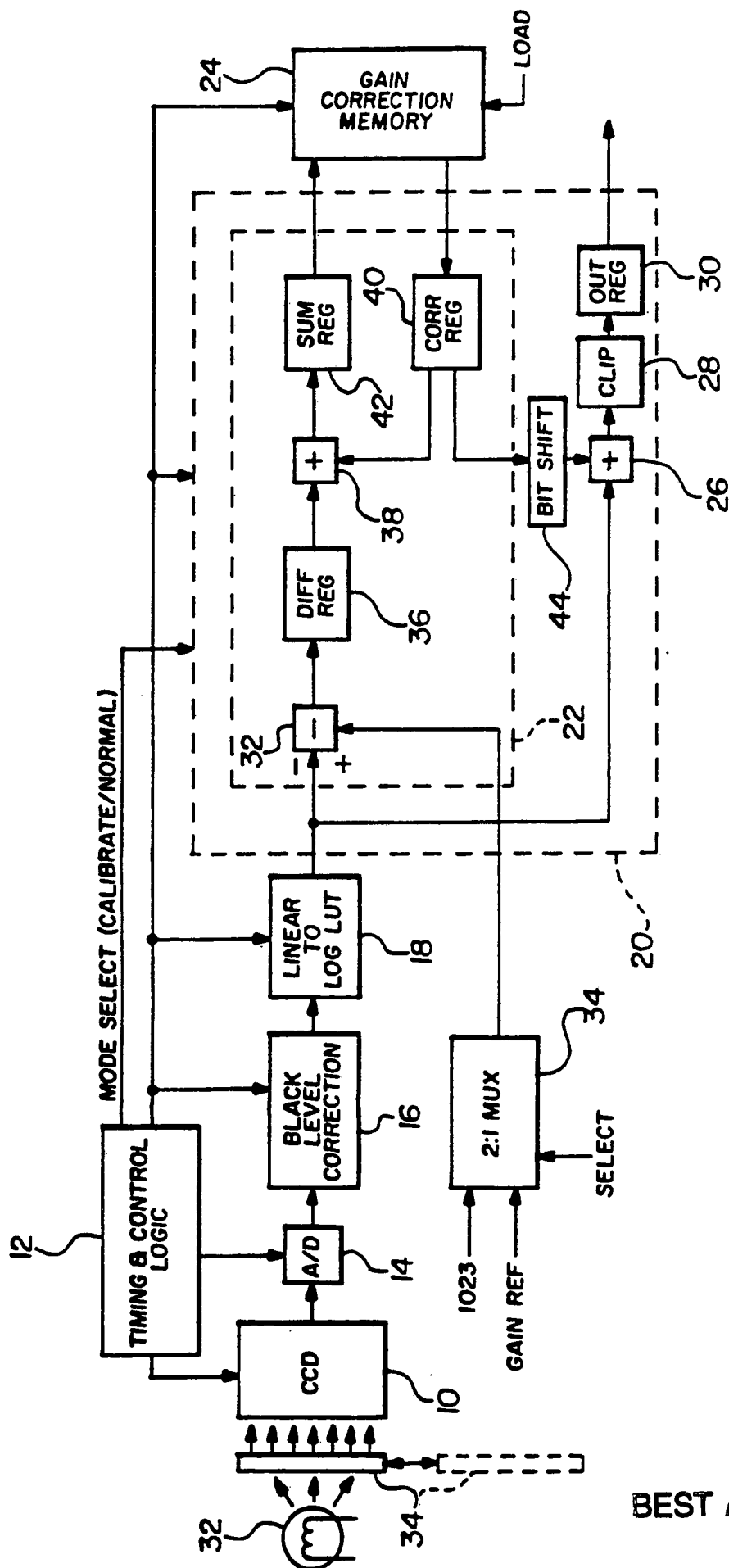
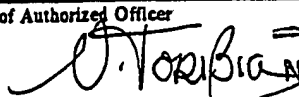


FIG. 1

<b>I. CLASSIFICATION OF SUBJECT MATTER</b> (If several classification symbols apply, indicate all) <sup>6</sup>		
According to International Patent Classification (IPC) or to both National Classification and IPC Int.C1.5                      H 04 N    1/40                      H 04 N    5/217		
<b>II. FIELDS SEARCHED</b>		
Minimum Documentation Searched <sup>7</sup>		
Classification System	Classification Symbols	
Int.C1.5	H 04 N	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched <sup>8</sup>		
<b>III. DOCUMENTS CONSIDERED TO BE RELEVANT<sup>9</sup></b>		
Category <sup>10</sup>	Citation of Document, <sup>11</sup> with indication, where appropriate, of the relevant passages <sup>12</sup>	Relevant to Claim No. <sup>13</sup>
X	US-A-4 885 467 (HORIKAWA) 5 December 1989, see the whole document	1, 2, 4-8
Y		3, 7, 9, 10
Y	US-A-4 888 492 (ARIMOTO) 19 December 1989, see figures 7, 11; column 7, line 59 - column 8, line 7	3, 7
Y	EP-A-0 357 084 (FUJI PHOTO FILM CO., LTD) 7 March 1990, see figure 2; column 1, lines 27-30; column 3, lines 12-32	9, 10
A		2, 5, 6, 8
X	US-A-4 783 836 (TAKASHIMA) 8 November 1988, see the whole document	1, 4
	-/-	
<p><sup>10</sup> Special categories of cited documents: <sup>10</sup></p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"&amp;" document member of the same patent family</p>		
<b>IV. CERTIFICATION</b>		
Date of the Actual Completion of the International Search	Date of Mailing of this International Search Report	
09-08-1991	20. 09. 91	
International Searching Authority	Signature of Authorized Officer	
EUROPEAN PATENT OFFICE	 Nuria TORIBIO	

III. DOCUMENTS CONSIDERED TO BE RELEVANT (CONTINUED FROM THE SECOND SHEET)		
Category *	Citation of Document, with indication, where appropriate, of the relevant passages	Relevant to Claim No.
A	---	2,3,5-8
X	PATENT ABSTRACTS OF JAPAN, vol. 10, no. 197 (E-418)[2253], 10 July 1986, & JP-A-61 39 776 (CANON INC.) 25 February 1986, see the whole abstract ---	1
A	US-A-4 829 379 (TAKAKI) 9 May 1989, see the whole document ---	2,5,6,8 -10
A	PATENT ABSTRACTS OF JAPAN, vol. 13, no. 548 (E-8569[3896], 7 December 1989, & JP-A-1 226 282 (SONY CORP) 8 September 1989, see the whole abstract -----	9,10



**ANNEX TO THE INTERNATIONAL SEARCH REPORT  
ON INTERNATIONAL PATENT APPLICATION NO.**

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned international search report.  
The members are as contained in the European Patent Office EDP file on 11/09/91  
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

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